

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for: **B. Sc. Hons Electronics I - Sem** Effective from: July 20, 2016

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Generic	English G1 104	Net Anal Guest 104	Net Anal Guest 104	<b>LUNCH BREAK</b>	Basic Electronics and Network Analysis Lab. Guest, NT, E-4 Elec. Lab 117			
<b>Tuesday</b>	Mathematics Foundation for Electronics Lab. PKS, MS, E-4, E-5 Elec. Lab 116 and 117					Generic			
<b>Wednesday</b>		English G1 104	Net Anal Guest 104	Net Anal Guest 104		Generic Lab. Theory/Tutorial			
<b>Thursday</b>	Generic	Math. Fnd. E-5 104	Math. Fnd. E-5 104	English RP 104					
<b>Friday</b>						Generic	English RP 104	Math. Fnd. E-4 104	Math. Fnd. E-4 104
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min duratior

PKS	Dr. Poonam Kasturi	E-1
NT	Dr. Neeraj Tyagi	E-2
AM	Dr. Anurag Mishra	E-3
MS	Dr. Manoj Saxena	E-4
G1		E-5
RP	P. Rohith - English	E-6

Convener

Principal

Time table

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for: **B. Sc. Hons Electronics III - Sem** Effective from: July 20, 2016

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Digital Electronics and Verilog Lab. PKS, MS, E-2 Elec. Lab 117				<b>LUNCH BREAK</b>	Generic	Digital PKS 115	Circuits MS 115	
<b>Tuesday</b>	Generic Lab. Theory/Tutorial					C Prog. - DS AM 115	C Prog. - DS AM 115		
<b>Wednesday</b>	Generic	Digital PKS 115	Circuits MS 115	Circuits MS 115		Skill Enhn. Lab. - Robotics E-6, E-4 Elec. Lab 117			
<b>Thursday</b>	Electronic Circuits Lab. PKS, MS, E-3 Elec. Lab 117					Generic	C Prog. - DS AM 115	C Prog. - DS AM 115	
<b>Friday</b>	Generic	Digital PKS 104	Digital PKS 104	Circuits MS 104		C Programming and Data Structures Lab. AM, E-1, E-5 Elec. Lab 117			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min duratior

PKS	Dr. Poonam Kasturi	E-1
NT	Dr. Neeraj Tyagi	E-2
AM	Dr. Anurag Mishra	E-3
MS	Dr. Manoj Saxena	E-4
		E-5
		E-6

Convener

Principal

Time table

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for: **B. Sc. Hons Electronics V - Sem**

Effective from: **July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>			Micropr. E-5 115	Micropr. E-5 115	<b>LUNCH BREAK</b>	Microprocessor and Microcontroller Lab AM, E-5, E-6 Elec. Lab 116			
<b>Tuesday</b>		Antenna E-6 115	Instru. E-4 115	Comm. NT 115		Communication Lab. NT, E-5, E-6 Elec. Lab 116 and 117			
<b>Wednesday</b>	Comm. NT 117	Comm. NT 117	Instru. E-4 117	Instru. E-4 117		Microprocessor and Microcontroller Lab AM, E-4, E-5 Elec. Lab 116			
<b>Thursday</b>	Instru. E-4 116	Instru. E-4 116	Comm. NT 116	Comm. NT 116		Micropr. E-5 104	Micropr. E-5 104	Antenna E-6 104	Antenna E-6 104
<b>Friday</b>	Communication Lab. NT, E-4, E-6 Elec. Lab 116					Antenna E-6 115	Antenna E-6 115	Micropr. E-5 115	
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

PKS	Dr. Poonam Kasturi	E-1
NT	Dr. Neeraj Tyagi	E-2
AM	Dr. Anurag Mishra	E-3
MS	Dr. Manoj Saxena	E-4
		E-5
		E-6

Convener

Principal

Time table

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for: **B. Tech Electronics VII - Sem**

Effective from: **July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Computer Networks Lab. E-1, E-3 Elec. Lab 116				<b>LUNCH BREAK</b>	Photonics E-2 104	Power E-1 104	Comp. Net. E-3 104	
<b>Tuesday</b>	Power E-1 104	Power E-1 104	Photonics E-2 104	Photonics E-2 104		Comp. Net. E-3 104	Comp. Net. E-3 104		
<b>Wednesday</b>	Power Electronics Lab. E-1, E-2 Elec. Lab 116					Comp. Net. E-3 104	Comp. Net. E-3 104		
<b>Thursday</b>	Photonics E-2 115	Photonics E-2 115	Power E-1 115	Power E-1 115		Project Time Slot			
<b>Friday</b>	Project Time Slot					Photonics Lab. E-2, E-3 Elec. Lab 116			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

PKS	Dr. Poonam Kasturi	E-1
NT	Dr. Neeraj Tyagi	E-2
AM	Dr. Anurag Mishra	E-3
MS	Dr. Manoj Saxena	E-4
		E-5
		E-6

Convener

Principal

Time table

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**Dr. Poonam Kasturi**

**Effective from: July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Digital Electronics and Verilog Lab. Elec III Sem Elec. Lab 117				<b>LUNCH BREAK</b>		Digital Elec III Sem 115		
<b>Tuesday</b>	Mathematics Foundation for Electronics Lab. Elec I Sem Elec. Lab 116 and 117								
<b>Wednesday</b>		Digital Elec III Sem 115							
<b>Thursday</b>	Electronic Circuits Lab. Elec III Sem Elec. Lab 117								
<b>Friday</b>		Digital Elec III Sem 104	Digital Elec III Sem 104						
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min duratiior

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**Dr. Manoj Saxena**

**Effective from: July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Digital Electronics and Verilog Lab. Elec III Sem Elec. Lab 117				<b>LUNCH BREAK</b>			Circuits Elec III Sem 115	
<b>Tuesday</b>	Mathematics Foundation for Electronics Lab. Elec I Sem Elec. Lab 116 and 117								
<b>Wednesday</b>			Circuits Elec III Sem 115	Circuits Elec III Sem 115					
<b>Thursday</b>	Electronic Circuits Lab. Elec III Sem Elec. Lab 117								
<b>Friday</b>				Circuits Elec III Sem 104					
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min duratiior

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLGE (UNIVERSITY OF DELHI)**

Time table for: **Dr. Neeraj Tyagi** Effective from: July 20, 2016

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>					<b>LUNCH BREAK</b>	Basic Electronics and Network Analysis Lab. Elec I Sem Elec. Lab 117			
<b>Tuesday</b>				Comm. Elec V Sem 115		Communication Lab. Elec V Sem Elec. Lab 116 and 117			
<b>Wednesday</b>	Comm. Elec V Sem 117	Comm. Elec V Sem 117							
<b>Thursday</b>			Comm. Elec V Sem 116	Comm. Elec V Sem 116					
<b>Friday</b>	Communication Lab. Elec V Sem Elec. Lab 116								
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLGE (UNIVERSITY OF DELHI)**

Time table for: **Dr. Anurag Mishra** Effective from: July 20, 2016

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>					<b>LUNCH BREAK</b>	Microprocessor and Microcontroller Lab Elec V Sem Elec. Lab 116			
<b>Tuesday</b>						C Prog. - DS Elec III Sem 115	C Prog. - DS Elec III Sem 115		
<b>Wednesday</b>						Microprocessor and Microcontroller Lab Elec V Sem Elec. Lab 116			
<b>Thursday</b>							C Prog. - DS Elec III Sem 115	C Prog. - DS Elec III Sem 115	
<b>Friday</b>						C Programming and Data Structures Lab. Elec III Sem Elec. Lab 117			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**E-1**

**Effective from: July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Computer Networks Lab. B. Tech VII Elec. Lab 116				<b>LUNCH BREAK</b>		Power B. Tech VII 104		
<b>Tuesday</b>	Power B. Tech VII 104	Power B. Tech VII 104							
<b>Wednesday</b>	Power Electronics Lab. B. Tech VII Elec. Lab 116								
<b>Thursday</b>			Power B. Tech VII 115	Power B. Tech VII 115		Project Time Slot B. Tech VII			
<b>Friday</b>						C Programming and Data Structures Lab. Elec III Sem Elec. Lab 117			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**E-2**

**Effective from: July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Digital Electronics and Verilog Lab. Elec III Sem Elec. Lab 117				<b>LUNCH BREAK</b>	Photonics B. Tech VII 104			
<b>Tuesday</b>			Photonics B. Tech VII 104	Photonics B. Tech VII 104					
<b>Wednesday</b>	Power Electronics Lab. B. Tech VII Elec. Lab 116								
<b>Thursday</b>	Photonics B. Tech VII 115	Photonics B. Tech VII 115							
<b>Friday</b>	Project Time Slot B. Tech VII					Photonics Lab. B. Tech VII Elec. Lab 116			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLGE (UNIVERSITY OF DELHI)**

Time table for: **E-3** Effective from: July 20, 2016

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Computer Networks Lab. B. Tech VII Elec. Lab 116				<b>LUNCH BREAK</b>			Comp. Net. B. Tech VII 104	
<b>Tuesday</b>						Comp. Net. B. Tech VII 104	Comp. Net. B. Tech VII 104		
<b>Wednesday</b>						Comp. Net. B. Tech VII 104	Comp. Net. B. Tech VII 104		
<b>Thursday</b>	Electronic Circuits Lab. Elec III Sem Elec. Lab 117					Project Time Slot B. Tech VII			
<b>Friday</b>			Project Time Slot B. Tech VII			Photonics Lab. B. Tech VII Elec. Lab 116			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min duratior

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLGE (UNIVERSITY OF DELHI)**

Time table for: **E-4** Effective from: July 20, 2016

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>					<b>LUNCH BREAK</b>	Basic Electronics and Network Analysis Lab. Elec I Sem Elec. Lab 117			
<b>Tuesday</b>	Math. Lab. Elec I Sem Elec. Lab 116 and 117		Instru. Elec V Sem 117						
<b>Wednesday</b>			Instru. Elec V Sem 117	Instru. Elec V Sem 117		Skill Lab. - Robotics Elec III Sem Elec. Lab 117		Microproc. Lab Elec V Sem Elec. Lab 116	
<b>Thursday</b>	Instru. Elec V Sem 116	Instru. Elec V Sem 116							
<b>Friday</b>	Communication Lab. Elec V Sem Elec. Lab 116							Math. Fnd. Elec I Sem 104	Math. Fnd. Elec I Sem 104
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min duratior

Convener  
Time table

Principal

**DEEN DAYAL UPADHYAYA COLLGE (UNIVERSITY OF DELHI)**

Time table for:

**E-5**

**Effective from: July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>			Micropr. Elec V Sem 115	Micropr. Elec V Sem 115	<b>LUNCH BREAK</b>	Microproc. Lab Elec V Sem Elec. Lab 116			
<b>Tuesday</b>			Math. Lab. Elec I Sem Elec. Lab 116 and 117			Communication Lab. Elec V Sem Elec. Lab 116 and 117			
<b>Wednesday</b>						Microprocessor and Microcontroller Lab Elec V Sem Elec. Lab 116			
<b>Thursday</b>		Math. Fnd. Elec I Sem 104	Math. Fnd. Elec I Sem 104			Micropr. Elec V Sem 104	Micropr. Elec V Sem 104		
<b>Friday</b>						C Prog.Lab. Elec III Sem Elec. Lab 117		Micropr. Elec V Sem 115	
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

Convener

Principal

Time table

**DEEN DAYAL UPADHYAYA COLLGE (UNIVERSITY OF DELHI)**

Time table for:

**E-6**

**Effective from: July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>					<b>LUNCH BREAK</b>	Microprocessor and Microcontroller Lab Elec V Sem Elec. Lab 116			
<b>Tuesday</b>		Antenna Elec V Sem 115				Communication Lab. Elec V Sem Elec. Lab 116 and 117			
<b>Wednesday</b>						Skill Lab. - Robotics Elec III Sem Elec. Lab 117			
<b>Thursday</b>								Antenna Elec V Sem 104	Antenna Elec V Sem 104
<b>Friday</b>		Communication Lab. NT, E-4, E-6 Elec. Lab 116				Antenna Elec V Sem 115	Antenna Elec V Sem 115		
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

Convener

Principal

Time table

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**Guest**

*Against Dr. Ravinder Kaur*

**Effective from: July 20, 2016**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>			Net Anal Elec I Sem 104	Net Anal Elec I Sem 104	<b>LUNCH BREAK</b>	Basic Electronics and Network Analysis Lab. Elec I Sem Elec. Lab 117			
<b>Tuesday</b>									
<b>Wednesday</b>			Net Anal Elec I Sem 104	Net Anal Elec I Sem 104					
<b>Thursday</b>									
<b>Friday</b>									
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No change in Time table is possible without the permission of the Principa

For students of Semester V, all periods of 55 min durati

Convener  
Time table

Principal